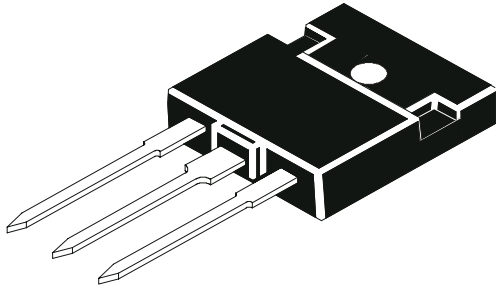


**NPN SILICON POWER TRANSISTOR**

**CSC5200F  
TO-3P**



**POWER AMPLIFIER APPLICATIONS.  
Complementary CSA1943F**

**MAXIMUM RATINGS(Ta=25 deg C)**

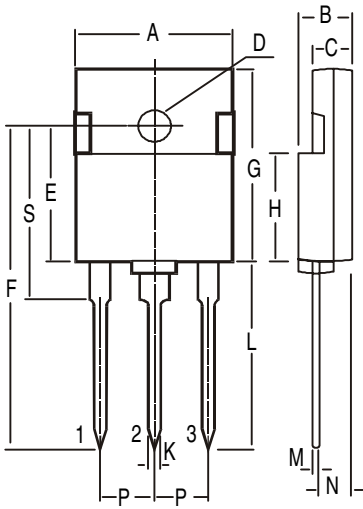
DESCRIPTION	SYMBOL	VALUE	UNIT
Collector -Base Voltage	VCBO	160	V
Collector -Emitter Voltage	VCEO	160	V
Emitter -Base Voltage	VEBO	5.0	V
Collector Current	IC	12	A
Base Current	IB	1.2	A
Power Dissipation @ Tc=25 deg C	PC	90	W
Operating And Storage Junction Temperature Range	Tj, Tstg	-55 to +150	deg C

**ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)**

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector-Cutoff Current	ICBO	VCB=160V, IE=0	-	-	5.0	uA
Emitter-Cutoff Current	IEBO	VEB=5V, IC=0	-	-	5.0	uA
Collector -Emitter Voltage	VCEO	IC=50mA, IB=0	160	-	-	V
DC Current Gain	hFE	IC=1A, VCE=5V	55	-	160	
		IC=7A, VCE=5V	35	-	-	
Collector Emitter Saturation Voltage	VCE(Sat)	IC=8A, IB=0.8A	-	-	3.0	V
Base Emitter on Voltage	VBE(on)	IC=7A, VCE=5V	-	-	1.5	V
Transition Frequency	ft	VCE=5V, IC=1A	-	30	-	MHz
Collector Output Capacitance	Cob	VCB=10V, IE=0 f=1MHz	-	200	-	pF

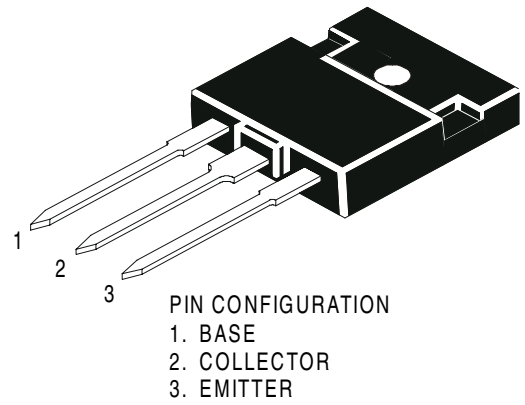
**\*hFE Classification : R : 55-110, O : 80-160**

## TO-3P (TO-218) Plastic Package



All dimensions in mm.

DIM	MIN.	MAX.
A	15.80	16.40
B	5.20	5.70
C	3.80	4.20
D	Ø3.30	Ø3.60
E	14.50	15.10
F	33.25	36.75
G	20.75	21.25
H	11.50	12.25
K	1.00	1.30
L	18.75	21.65
M	0.40	0.60
N	3.15	3.45
P	5.21	5.72
S	18.75	19.25



TO BE MOUNTED WITH SILICONE GREASE ON THE BACK SIDE,  
 ENSURING BACK SIDE HOLES FILLED.

### Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-3P	100 pcs/polybag	628 gm/100 pcs	3" x 7.5" x 7.5"	0.3K	17" x 15" x 13.5"	4.8K	42 kgs

### Notes

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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